



1503.64981

PATENT APPLICATION

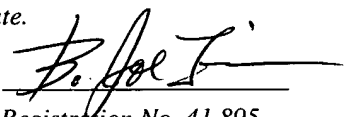
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hiroshi Kondo
Serial No.: 09/723,768
Conf. No.: 6836
Filed: 11/28/2000
For: APPARATUS AND METHOD
FOR PREVENTING AN
ERRONEOUS OPERATION AT
THE TIME OF DETECTION OF
A SYSTEM FAILURE
Art Unit: 2113
Examiner: Puente, Emerson C.

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

October 28, 2004

Date


Registration No. 41,895
Attorney for Applicant(s)

AMENDMENT B

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed June 16, 2004, please amend the application as follows: